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Characteristics of the Radiation Damage Seen in the Silicon Microstrip Detector of Fermilab Experiment E771

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ABSTRACT

The central region of the silicon microstrip detector used in Fermilab experiment E771 was subjected to a peak fluence of $9.5 \times 10^{13} \, \mathrm{p/cm^2}$ induced by 800 GeV protons over a two month period. Fourteen 300 $\mu \mathrm{m}$ thick planes manufactured by Micron Semiconductor were operated at bias voltages ranging from 84 to 109 V. Analysis of data from low intensity beam triggers taken near the end of the run shows that the mean pulse height from our amplifiers began to decline at a fluence of approximately $2 \times 10^{13} \, \mathrm{p/cm^2}$ and fell to near zero by $6 \times 10^{13} \, \mathrm{p/cm^2}$. We show that the use of fast amplifiers contributed to this early loss of signal.

1. Introduction

There have been several reported studies[1-7] on the deterioration of n-type silicon detectors after irradiation by high energy hadron beams with exposures of approximately 10^{14} p/cm². These beams produce significant displacement and transmutation damage in the silicon lattice, and have been shown[1] to convert wafers similar to ours from n-type silicon to p-type at a fluence of about 10^{13} p/cm². The bulk features of depletion depth and leakage current density are well understood, and studies[8] of the signal

shape from new and irradiated diodes[9] have also been reported, but there has apparently been no study of the signal from a fast amplifier responding to a fine pitch strip detector after irradiation has produced type-inversion.

As exposure levels rise above 10^{13} p/cm², it is known that higher bias voltages are needed to achieve full depletion of the silicon wafer, but there are apparent disagreements in the literature as to what constitutes an adequate bias for a 300 μ m thick wafer at 20° C. Recent capacitance measurements[1, 7] of silicon wafers indicate that a bias voltage of 100 V will fully deplete our wafers up to about 4×10^{13} p/cm², but earlier studies[2, 4, 5] reported little loss of pulse height after exposures in excess of 10^{14} p/cm² with thickness scaled bias voltages in the range of 90-135 V.

The experience of the E771 experiment is that, in the area near the beam axis, our signal began to deteriorate very abruptly at about $2 \times 10^{13} \text{ p/cm}^2$ to the point that the detector became essentially inoperative at $6 \times 10^{13} \text{ p/cm}^2$. This suggests that our nominal 100 V bias was not sufficient to adequately deplete the wafers in the region of highest irradiation, but also indicates that some additional factor is needed to explain our early loss of signal amplitude.

In this paper we report on a study of the amplitude of the signal from our amplifiers in response to incident beam protons. The data used were taken in low intensity beam runs near the end of the experiment under otherwise normal running conditions. We show that the observed loss of signal amplitude after type inversion can be attributed to the use of fast amplifiers in conjunction with a fine pitch strip detector.

2. Running Conditions

Fermilab experiment E771[10] exposed eighteen new planes of a silicon microstrip detector (SMD) to a beam of 800 GeV protons. All planes of the SMD used n-type silicon wafers with p-type strips and were operated near a temperature of 20° C. There were six beam planes upstream of the target, the first four were manufactured by Hamamatsu on 250 μm thick wafers and had 250 μm pitch strips ; the other two beam planes and the twelve downstream planes were manufactured by Micron Semiconductor on 300 μm thick wafers. The Micron planes are 5 cm \times 5 cm in transverse dimension

and were of two types: K and L. The K type planes have 384 25 μm wide strips on 50 μm pitch centers in the middle and 304 75 μm wide strips on 100 μm pitch centers on the outer edges . The L type planes were similar except with 15 μm strips on 25 μm pitch centers in the middle and 25 μm wide strips on 50 μm centers at the edges for the same number of strips but covering a smaller area. The beam was confined almost entirely to the 25 μm and 50 μm pitch sections of all planes. The arrangement, type, and numbering of the planes is shown in Fig. 1.

By the end of the run the Hamamatsu beam planes were being operated at bias voltages in the range 110-120 V, while the post-target Micron planes which are the focus of this study were at 84-109 V, several times the initial full depletion voltage of each plane and at or above the breakdown voltage rating given by the manufacturer.

Because only a small part of each strip was exposed to high fluence, leakage currents remained relatively low. At the end of the run we observed maximum strip leakage currents of less than 200 nA, well within the acceptable range for our preamps which were fast bipolar transimpedance amplifiers developed on an ASIC by the Research Division at Fermilab[11] These amplifiers featured an impulse response with a full width at half-maximum of about 15 ns, and were directly coupled to the strips, effectively grounding them through $200~\Omega$ resistances.

We estimate that the total beam exposure was 5×10^{13} protons and that secondary particles produced in the target foils contributed an additional 36% to the total flux seen by the silicon wafers. Most of this exposure came during the last month of running when the beam intensity averaged 0.8×10^9 protons/pulse. The integrated beam-plus-secondaries profile σ 's in planes X4 and Y4 at the end of the run were 0.39 and 0.29 cm as determined by Gaussian fits to the corresponding leakage current profiles measured within a month of the conclusion of the run. With these profiles, we estimate a peak accumulated fluence downstream of the target of $9.5\pm 3\times 10^{13}$ p/cm² at beam center which was accumulating at the rate of 2.2×10^{12} p/cm² per delivered beam-day during the last month. The uncertainty in the value of the peak fluence is due to a number of uncertainties in determining the total integrated beam seen by the detector. In the following results this

uncertainty is displayed as a systematic error. We use the same fluence distribution for all planes downstream of the target, ignoring the dispersion of target secondaries and the accumulation of additional secondaries from interactions in the detector planes. None of the results described below has a significant correlation with plane position along the beam direction.

At several times during this experiment we collected data with low intensity beam triggers defined by a scintillator that was placed into the beam only for these runs. We previously reported[12] that 2D scatterplots of the intersections of X4,Y4,U1 hits in events from low intensity beam triggers taken near the end of the run showed an obvious lack of points near beam center. A similar plot made from events with unambiguous crossings of position-matched hits in the X1,X2 and in the Y1,Y2 Hamamatsu beam planes showed a similar result but exhibited the loss of hits in a smaller region around the beam center.

The voltage output pulse from our amplifiers was converted to a digital signal by a time-over-threshold post-amp-comparator which had a 10 mV hysteresis. The threshold of the comparator was set by a DAC output. The correspondence between DAC setting and the threshold voltage is shown in Fig. 2. In the region of interest the relationship is approximately linear at 0.29 mV per DAC count. On occasion we ran threshold studies using the low intensity beam triggers, varying the threshold from 40 to 90 DAC counts in ten count steps. Threshold settings during normal data acquisition at the end of the run were in the low 40's.

3. Data Analysis

Since the X1, X2, Y1, and Y2 beam planes are less affected [13] in the central region than the Micron planes, we are able to use these four planes to define beam tracks that can be used to determine the efficiency of each of the Micron planes as a function of x and y. Fig. 3 shows the efficiency of 1 mm squares of plane X4 determined in this way. This plot was made from low intensity beam triggers taken near the end of the run and is overlaid with contours of constant fluence as determined from the leakage current x and y profiles in planes X4 and Y4 as measured within a month after the run.

An estimate of the pulse height distribution is necessary to make the conversion from efficiency to mean pulse height. If the pulse height distribution is represented by a Gaussian g normalized to area one and T is the threshold, the efficiency E is given by

$$E(T, \overline{h}, \sigma) = \int_{T}^{\infty} g(\overline{h}, \sigma; h) dh .$$
 1

If σ and T are known, this equation can be inverted and solved for the mean pulse height \overline{h} in terms of a measured efficiency.

A DAC threshold scan made early in the run was used to study the validity of the Gaussian assumption and to determine σ for the Gaussian representing the pulse height distribution. The run chosen shows very little evidence of radiation damage at the normal operating threshold, but the central dip in efficiency becomes quite clear at higher threshold settings. To make this study, we use beam tracks traversing the wafer through two narrow bands spanning the central region from -1 cm to +1 cm and containing the point of maximum fluence: one band is 4 mm wide and parallel to the x axis and the other is 5 mm wide and parallel to the y axis. The location of these bands on the wafer is shown in Fig. 3. Each band is transected into twenty equal rectangular areas so that there is little fluence variation across each area.

Using beam tracks defined by the Hamamatsu planes, we determine the efficiency of each rectangular area of each of the planes X4-X8 and Y4-Y8 at each of six threshold settings. For each area of each plane these efficiencies are plotted against DAC threshold as shown by the examples in Fig. 4 for six representative areas in the strip along the x-axis of plane X4. With the threshold T as the independent variable, efficiency Eq. 1 is fit to each of these plots, yielding an \overline{h} and σ for each rectangle. These studies showed the pulse height distribution could be well represented by a Gaussian of essentially constant σ , independent of \overline{h} . A constant σ is qualitatively consistent with the expectations for electronic noise originating in the preamps and comparator. The error weighted average of σ was 23.7 for the X-planes and 23.3 for the Y-planes with r.m.s. variation of 1.2 and 1.0

respectively. In the following analysis we fix σ to one of these two values depending on the plane orientation.

The curves in Fig. 4 were determined by fixing σ at 23.7 DAC units and fitting Eq. 1 to determine the mean pulse height \overline{h} in each rectangle. We cannot determine the integrated beam profile at this intermediate point in the run, but this figure shows that the fluence was already sufficient to produce a decrease in \overline{h} near beam center.

Using the data from low intensity beam triggers taken at the end of the run, we next determine the efficiency of 1 mm² areas of each X and Y plane as in the X4 plane example shown in Fig. 3. Each of these efficiencies is then converted to a mean pulse height \overline{h} using the appropriate average σ and threshold setting T. Fig. 5 shows the summarized results for each plane binned by fluence. Each mean pulse height on these plots is determined from the error weighted average of the \overline{h} for areas lying within fluence contours like those shown in Fig. 3.

All planes show essentially the same behavior of a sharp break in the mean pulse height followed by an almost linear decline. The upturn in the measured pulse height at higher fluence on some of the planes (and in the center of Fig. 3) is believed to be an artifact caused by the low probability of detecting a true beam track in the highest fluence regions of the Hamamatsu planes.

The curve on each plot of Fig. 5 is from a three parameter fit of the mean pulse height p in terms of the fluence Φ using the function

$$p = p_0 \text{ for } \Phi \le \Phi_c$$

$$p = p_0 \left(1 - \frac{\Phi - \Phi_c}{2(\Phi_h - \Phi_c)} \right) \text{ for } \Phi \ge \Phi_c, p > 0$$

$$p = 0 \text{ otherwise}$$
(2)

where p_0 is the initial height, Φ_c is the fluence where the pulse height starts to decline, and Φ_h the fluence where it has declined to half of its initial value. There are significant plane to plane variations in both Φ_c and Φ_h , but their mean values are 2.1 and 4.3×10^{13} p/cm² respectively.

We have been able to reproduce the main characteristics of this mean pulse height behavior by combining the known radiation effects in silicon with a calculation of the signal current collected on a strip of the detector. Initially, the bulk material in our silicon wafers was lightly doped to form a high resistance n-type semiconductor with heavily doped p-type implants forming the strips. After irradiation, the effective dopant density $N_{\rm D}$ can be described by the equation[7]

$$N_{\rm D} = -N_0 e^{-c\Phi} + \beta \Phi , \qquad (3)$$

in which N_0 is the initial density of n-type dopant, c describes the loss of this dopant, and β the generation of p-type dopants. The depletion voltage for a wafer is given by

$$V_{\rm D} = \frac{q|N_{\rm D}|w^2}{2\varepsilon} \quad , \tag{4}$$

where q is the electron charge, w is the thickness of the detector, and ε the dielectric constant for silicon (1.054 pF/cm). Combining Eqs. 3 and 4 yields an expression for the change in depletion voltage with fluence.

$$V_{\rm D} = \frac{q w^2}{2\varepsilon} \left| -N_0 e^{-c\Phi} + \beta \Phi \right| \quad . \tag{5}$$

The parameter c has a typical value near 1.5×10^{-13} cm² and is important only at lower fluences. The more important quantity is β which has been variously reported in the literature[1, 2, 6, 7, 14] as having values between 0.018 and 0.05 cm⁻¹. The initial depletion voltages of our planes varied from 21 to 36 V, so our initial depart density N_0 was about 4.0×10^{11} cm⁻³.

For a typical detector, the type inversion described by Eq. 3 occurs at about 1×10^{13} p/cm², and above this fluence any undepleted zone begins to form from the p-side of the detector creating a field free layer of conducting material. But even in a fully depleted wafer with $V_{\rm bias} > V_{\rm D}$, type-inversion changes the gradient of the applied field E(z) as indicated in Figure 6a, lowering the field in the vicinity of the strips.

A minimum ionizing particle passing through the detector creates a thin line of hole-electron pairs that immediately begin to drift apart under the influence of the applied field. Neglecting diffusion, the drift velocity of a free charge in the bulk silicon is given by

$$\mathbf{v}_{c}(z) = \mu_{c} \mathbf{E}(z), \tag{6}$$

where μ_c is the mobility of the charge carriers. Electrons drift about three times faster than holes, and after type-inversion both charge carriers move slower near the collection strips where $\mathbf{E}(z)$ is smallest. This effect is most significant for the slower moving holes which drift toward the strips, slowing further and concentrating as they move into regions of lower field. Although the same amount of charge is ultimately collected from a strip, this change in drift velocity with increasing fluence broadens the current pulse and reduces its amplitude.

The weighting field method[15, 16] can be used to make a quantitative estimate of the current induced on the collection electrode. In this analysis, we are concerned only with beam tracks which are parallel to the z-axis. Simplifying further to consider only charge pairs produced along a line centered on the electrode, the electrode current is given by

$$i(t) = \left| \frac{q}{V_1} \mathbf{E}_{\mathbf{w}}(z(t)) \cdot \mathbf{v}_{\mathbf{c}}(z(t)) \right| , \qquad (7)$$

where the weighting field $\mathbf{E}_{\mathbf{w}}$ is determined by fixing the collection electrode to voltage V_1 (typically 1 V) and grounding all others. For a diode of wafer thickness w, $|\mathbf{E}_{\mathbf{w}}| = V_1/w$.

We have determined the weighting field[17] for 25 and 50 μ m pitch strip detectors (15 and 25 μ m strip widths respectively) from a numerical solution[18] of Laplace's equation on a 60 by 180 grid with 5 μ m cells. All points on the boundary are set to zero potential except for the collection strip and the interval between it and the neighboring strips. In this between-strip interval the potential is assumed to fall linearly from unity to zero. Thus the nonzero portion of the 25 μ m pitch detector is given by the sequence 0.333, 0.667, 1.0, 1.0, 0.667, 0.333. The resulting weighting potential has a shape similar to that of the analytic solution for the more tractable boundary given in Ref. [19].

Along the electrode centered beam trajectory, we fit the numerical solution for the weighting potential to the form

$$V_{w}(z) = Az + \frac{B}{z - z_{0}} + \frac{C}{(z - z_{0})^{2}} + \frac{D}{(z - z_{0})^{3}}$$
 (8)

with $D = D(A, B, C, z_0, w)$ defined so that $V_w(w) = 1$ V. Differentiation gives an expression for the magnitude of the weighting field

$$E_{w}(z) = a + b(z-z_{0})^{-2} + c(z-z_{0})^{-3} + d(z-z_{0})^{-4}$$
(9)

with the coefficients for 25 and 50 µm pitches given in Table 1.

Table 1. Coefficients* of the weighting field

Pitch	а	b	с	d	z_0
25 μm	1.783×10 ⁻⁴	-3.0579	1484.3	28.637	326.44
50 μm	3.9281×10 ⁻⁴	-8.2226	4886.2	-2293.3	348.00

^{*} All units are volts and µm.

In Figure 6b we compare these weighting fields with the constant field of a diode detector. Note that after type-inversion the weighting field for a strip detector is largest where the drift speed is smallest; drifting holes concentrate in this region producing a late peaking contribution to the current signal.

Eq. 6 describes the charge drift velocity as a function of $\mathbf{E}(z)$, but to avoid infinite drift times in regions of zero field we impose a minimum drift velocity of $1 \,\mu\text{m/ns}$. The field $\mathbf{E}(z)$ can be described by the expression

$$|\mathbf{E}(z)| = \frac{V_{\text{bias}}}{d} - \frac{q}{\varepsilon} N_{\text{D}} \left(z - \frac{d}{2} \right) \quad , \tag{10}$$

where $V_{\rm bias}$ is the applied voltage, d is the thickness of depleted material, q is the electron charge, and ε the dielectric constant for silicon (1.054 pF/cm). In this expression z=0 on the n-side. The concentration of dopant atoms $N_{\rm D}$ is a signed number which is initially negative and changes to positive with increasing fluence according to the Eq. 3. For this calculation[20] we have used c and β values of 1.1×10^{-13} cm², and 0.037 cm⁻¹ respectively [7, 21].

Using the 25 μ m weighting field from Table 1, a bias of 100 V, and taking μ_c equal to 45 and 120 μ m²V⁻¹ns⁻¹ for holes and electrons

respectively[15], we numerically determine the current derived from a line of charge pairs which at t=0 are uniformly distributed along a beam trajectory in the undepleted thickness of the detector. Figure 7a shows a comparison of the charge collected on the electrodes of diode and strip detectors during 20 and 50 ns gates. Note that the 20 ns collection gate works fine for a diode or a new strip detector, but after type-inversion the charge collected from a strip starts to fall while $V_{\rm bias}$ is considerably greater than $V_{\rm D}$.

In Fig. 7b, we show the result of convoluting electrode current signals with the impulse response of our 15 ns amplifier[11] and a hypothetical 30 ns version. The predicted signal for a strip detector is seen to be very similar to the experimental result presented in Fig. 5. Further, the model predicts that the signal will lose approximately half its initial amplitude when the fluence has increased to the point that $V_D = V_{\rm bias}$. This result is not sensitive to changes in the constants c and β in Eq. 3 and changes only slightly for 50 μ m pitch strips.

Fig. 8 shows the values of Φ_c and Φ_h determined from the fits of Eq. 2 to the data for each of the ten planes as shown in Fig. 5. The values are plotted against the bias voltage used on each plane. The simple mean and standard deviation of the difference Φ_h . Φ_c for for all planes is $2.2 \pm 0.6 \times 10^{13}$ p/cm². The solid line in the figure is a one parameter fit of the expression

$$\Phi = \frac{2\varepsilon V_{\text{bias}}}{\beta a w^2} \,. \tag{11}$$

to the Φ_h points under the model inspired assumption that the mean pulse height has fallen to half of its initial value whdn $V_D = V_{bias}$. This expression is the inverse of Eq. 5 with the small exponential term neglected. The fit yields $\beta = 0.033 \pm 0.011$ cm⁻¹ where the quoted error comes from the systematic error on the total beam flux. There are significant variations between planes, and the two dashed lines show bounds that contain most of the planes: the limits are $\beta = 0.028$ and 0.040 cm⁻¹. Possible reasons for the variation between planes include cabling capacitance and timing.

4. Discussion

Although this determination of β is indirect and subject to a large systematic uncertainty, it is the first such measurement made after an exposure to a 800 GeV proton beam over a period of many weeks. The central value of β is larger than the values determined for 20° C wafers immediately after exposures of a few days. This may be partly due to higher levels of transmutational damage at our beam energy, but there may also be a connection with the annealing and anti-annealing processes[7] which occur with different time constants following few day exposures.

Since the most obvious way to increase the maximum fluence acceptable in the detector is to raise the bias voltage, we have run the model at 150 and 200 V biases. For a 25 μ m pitch detector and the 15 ns current amplifier, the model indicates that in spite of generally shorter drift times, the signal continues to fall to about one-half of its initial value at the point where $V_D = V_{\rm bias}$. However, the amplitude of the low fluence signal for these two voltages is larger than the 100 V value by 35 and 55% respectively.

We also note that the signal from a charge amplifier can be greatly improved by using a longer gate, but that the amplitude of the signal from a current amplifier is not similarly improved by doubling the width of its impulse response. This is a consequence of the late arrival of the drifting holes which have concentrated near the strip due to the drift speed gradient. The current from drifting electrons always peaks at t=0, but after type-inversion the hole signal develops a double peaked character that lowers, broadens, and delays the output signal from a current amplifier. The delay is especially pronounced for 25 μ m pitch strips.

5. Conclusions

We find that with a nominal 100 V bias, the output signal from our strip detector amplifiers began to decrease sharply at a fluence of about $2.5 \times 10^{13} \, \text{p/cm}^2$, and we have been able to understand this behavior in terms of recent measurements that quantify the rate of p-type dopant production combined with a detailed analysis of the signal generated by a single strip. Our analysis using the weighting field method indicates that with our amplifiers the output signal will fall to approximately half of its initial value

when $V_{\rm D} = V_{\rm bias}$, and that this model is consistent with our observations if the dopant production factor β is 0.033 cm⁻¹. The loss of signal while $V_{\rm D} < V_{\rm bias}$ is expected to be much less severe when the same amplifiers are connected to detectors with pad dimensions that are large compared to the wafer thickness.

Current amplifiers have the advantage of a fast recovery with no dead time, but it is apparent that they are also sensitive to the fluence dependent shape of the current signal from a strip detector. Increasing the bias voltage on the wafer reduces the drift time of charges (particularly the holes) at all fluences and can result in a larger signal from a current amplifier, but our model indicates that the signal will still be down by about 50% when $V_D = V_{\rm bias}$.

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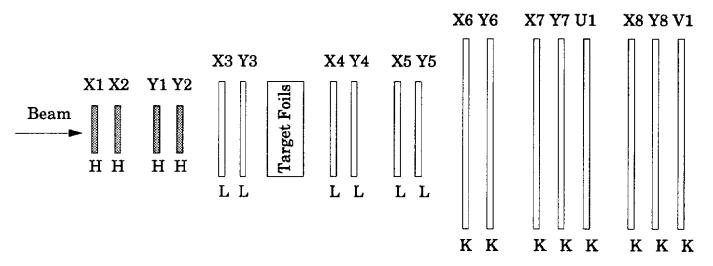
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Figures Captions

- Fig. 1. Plane layout of the E771 silicon microstrip detector.
- Fig. 2. Threshold voltage versus DAC setting.
- Fig. 3. Efficiency of small areas of the central region of plane X4 at the end of the run overlaid with contours of constant fluence at intervals of 0.1 of the maximum fluence. Small negative efficiencies result from the correction for random hits.
- Fig. 4. Each plot shows the efficiency versus DAC threshold in a small area of plane X4 at an early stage in the run. The fits are to a Gaussian pulse height distribution as described in the text. The mean pulse height corresponds to the inflection point of these curves.
- Fig. 5. Pulse height as a function of fluence for the ten post-target X and Y planes. The fluence on each plot is units of 10¹³ p/cm². The fitted lines are described in the text.
- Fig. 6. (a) The variation of the applied field across the thickness of the wafer as radiation damage increases from a to b. Note that after type-inversion the field strength is smallest near the strips located at $z=300~\mu m$. (b) The "weighting field" for a diode and for single strips from our 25 and 50 μm pitch detectors. This field is evaluated along a beam trajectory centered on the strip and is determined by applying 1 V to the collection electrode and grounding all others.
- Fig. 7. (a) The calculated fraction of the total charge collected in 20 ns and 50 ns gates as a function of fluence comparing a diode with a 25 μm pitch strip detector. (b) The relative amplitude of the output signal from our fast current amplifier and a slower one.

Fig. 8. Each solid marker shows the fluence Φ_h where the mean pulse height of a plane has fallen to 50% of its initial amplitude; the open markers show the fluence Φ_c defined by Eq. 2. Statistical errors are too small to display. The solid line is a one parameter straight line fit to the solid markers yielding $\beta = 0.033$, and the two dashed lines show $\beta = 0.028$ and $\beta = 0.040$ extremes. Square markers indicate x-planes, circles y-planes.



Not to scale

Figure 1

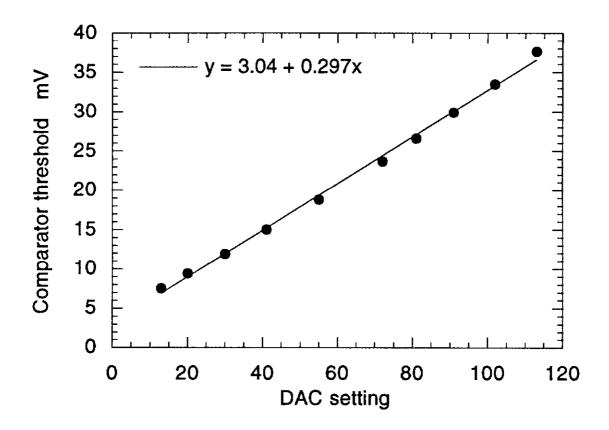


Figure 2

Efficiency for plane X4 Ë 100 50 33 50 100 100 100 66 66 66 50 100 0.75 y position 0.5 0.25 -0.25 -0.5 -50 -0.75 H00 100 25 100 66 50 100 -0.75 -0.5 -0.25 0.25 0.5 0.75 x position cm

Figure 3

Efficiency versus threshold, plane X4

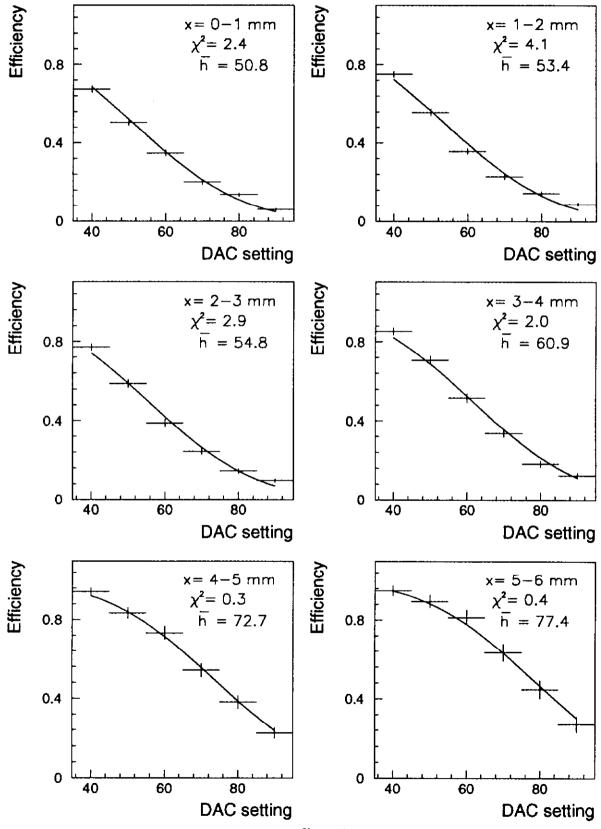
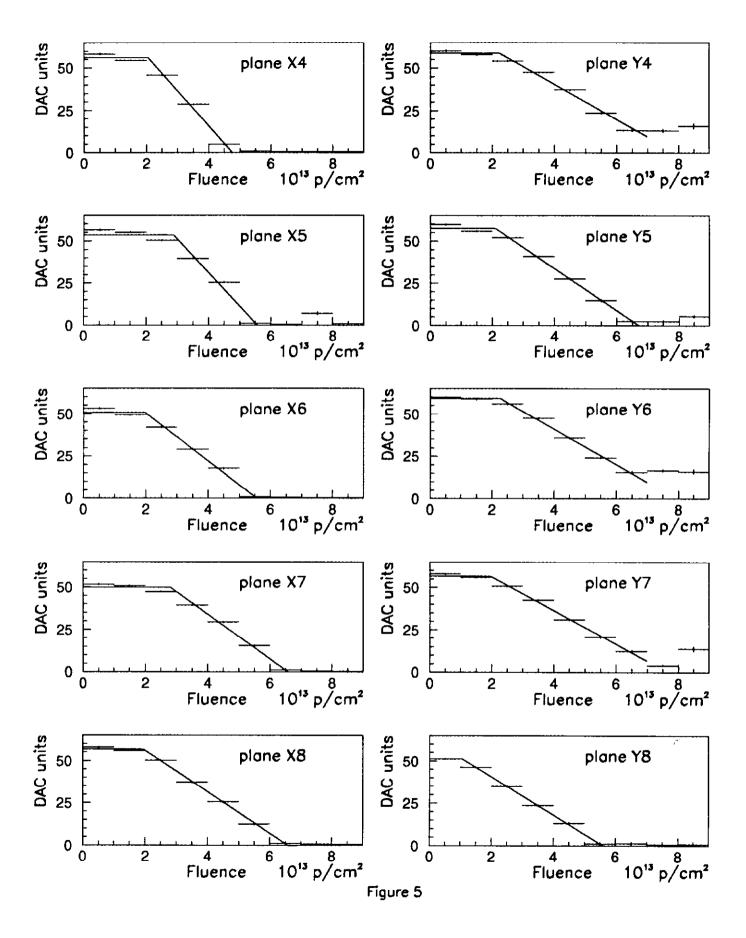
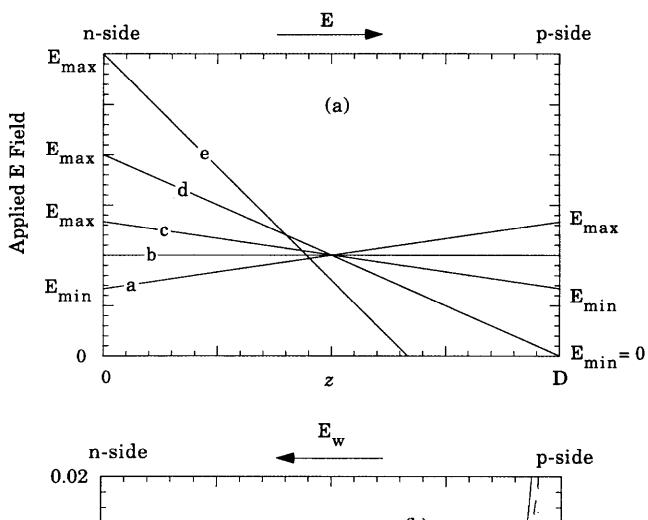


Figure 4





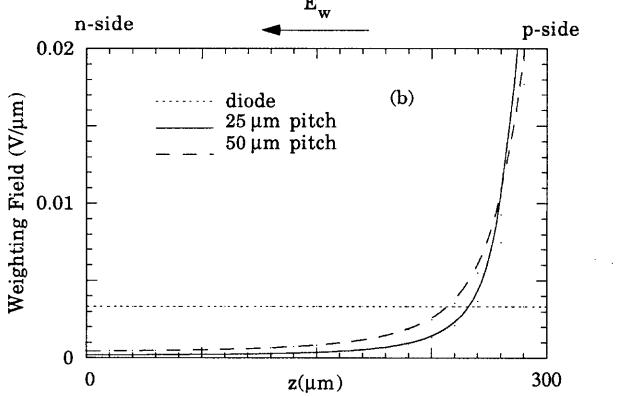


Figure 6

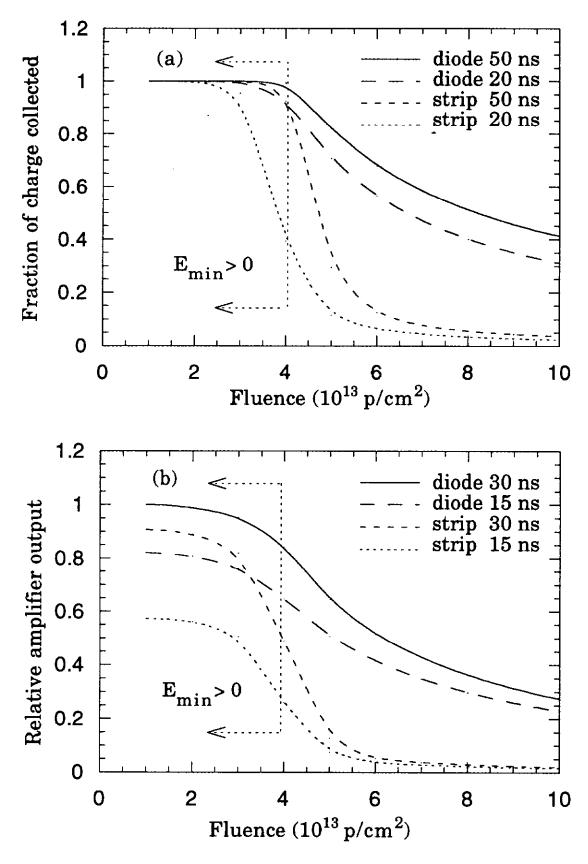


Figure 7

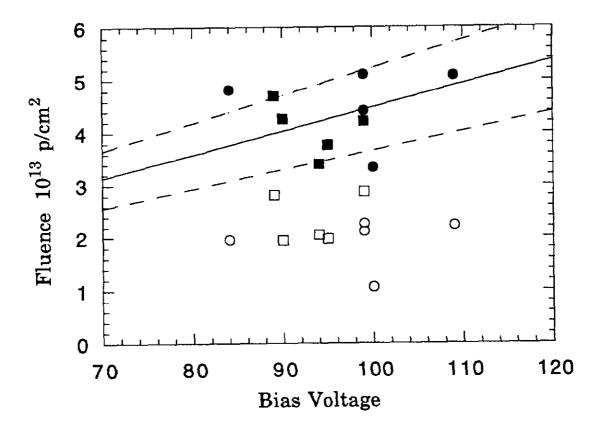


Figure 8